

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

### Title of Invention

METHOD FOR REDUCING SHALLOW TRENCH ISOLATION  
CONSUMPTION IN SEMICONDUCTOR DEVICES

Application Number :

Date :

First Named Applicant: Bruce B. Doris

Confirmation Number:

Attorney Docket Number: FIS920030256US1

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Documents being submitted:		Files
us-ids	us-ids	FIS920030256US1-usidst.xml us-ids.dtd us-ids.xsl
us-request	us-request	FIS920030256US1-usrequ.xml us-request.dtd us-request.xsl
us-assignment	us-assignment	FIS920030256US1-usassn.xml us-assignment.xsl us-assignment.dtd FIS920030256US1Assignment1.tif FIS920030256US1Assignment2.tif
us-fee-sheet	us-fee-sheet	FIS920030256US1-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	us-declaration	FIS920030256US1Declaration1.tif
us-declaration	us-declaration	FIS920030256US1Declaration2.tif
us-declaration	us-declaration	FIS920030256US1Declaration3.tif
application-body	application-body	FIS920030256US1-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblx.dtd FIS920030256US1Table7.tif FIS920030256US1Drawing1.tif FIS920030256US1Drawing2.tif FIS920030256US1Drawing3.tif

**Comments**

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